

Self-Appraisal

Section- I: Basic/General/Personal Information

- 01. Name** : T. D. SHASHIKALA
- 02. Age (in years)** : 45
- 03. Designation and Department** : Associate Professor, Department of Electronics and communication Engineering (Aug 1997 -Jan 2018)
Associate Professor in DOS&R, Kuvempu University (Jan 2018 – till Date)

04. Education Qualification including Diplomas and Highest Degree

Degree/Diploma	University/Institution	State	Year
B.E.	Kuvempu University / JNNCE, Shimoga	Karnataka	1994
M.Tech	VTU, Belgaum/BMSCE, Bangalore	Karnataka	2009
PhD	VTU Belgaum/Univerity BDT college of Engg	Karnataka	Course work completed

Section- II: Teaching Experience

05. Teaching Experience (before joining Kuvempu University)

Position Held	Nature of Appointment (Permanent/ Temporary)	Duration		Level (PG/ UG)	No. of Years	University/ Institution
		From	To			
Lecturer	Temporary	Jan1995to	April1997	UG	2Years	College of Agri. Science Shimoga,JNNCE Shimoga

06. Date of joining Kuvempu University: 27-08-1997

07. Teaching Experience in Kuvempu University

Position	Academic Programs Taught	Courses Taught	Academic Year	Number of Years
Lecturer	UG	AEC-I, DEC, Control systems, Antenna & Propagation, Transmission lines, C programming, Communication systems,	Aug 1997 to March 2003	8
Senior lecturer			March 2003 to August 2008	3
Selection grade lecturer	UG & PG	Optic Fiber Communication, Digital Communication, Wireless Communication, advanced Digital systems, Satellite Communication, Basic Electronics. Advanced digital communication. Optical networks, signals and systems, electronic Instrumentation, Measurements, Image Processing, Electromagnetic wave theory and RADAR systems.	August 2008 to July 2012	4
Associate Professor			July 2012 to till date	6

08. Innovations in, and Contribution to, Teaching (Feb 2003 to Feb 2011)

Kinds of Teaching-related Activities	Year	Nature of your Contribution
a. Design of Curriculum	2002&2007	Design of Scheme & Framing of Syllabus for RNS and CBCS scheme
b. Teaching Methods	2002-07	Black Board
c. Laboratory Experiments	2002-07	Analog Electronics Lab, Advanced Digital Communication Lab, wireless communication lab, Microprocessor Lab, Communication Systems-I Lab, Power Electronics Lab, VHDL and Veri log .
d. Evaluation Methods	2002-07	Quizzes, Seminar, Tests, Assignments, Mini Project
e. Preparation of Resource Material Including Books, Reading Materials, Laboratory Manuals, etc.,	2008	Analog electronics –I Lab manual Advanced communication systems lab manual
f. Remedial Teaching/ Student Counseling (Academic)		Mentor –Every year 10 Students

10. Details of Training /Faculty Improvement/Professional Training/Further Study Undergone (Feb 2003 to Feb 2011)

SINo	Nature of Programme	Period		Institution
		From	To	
1	STTP on Recent Advances in Bio-Medical Engineering	29 th July	9 th August 2002	B.I.E.T Davangere
2	Strategic planning for institutional development	2 nd	13 th Dec 2002	DRR (Govt) Polytechnic, Davanagere.
3	5 th State Level ISTE Convention and National seminar on “Role of Technical Education in meeting the Challenges of globalization”	12 th	13 th Nov 2002	U.B.D.T.C.E. Davangere
4	Workshops on Microelectro Mechanical Systems (MEMS)	7 th	8 th Nov 2003.	BVBCE Hubli
5	STTP on Fuzzy Logic and its Applications to Power Systems	15 th	26 th March 2004	B.I.E.T Davangere
6	Trainers Training Programme	9 th	10 th Oct 2006	U.B.D.T.C.E. Davangere
7	Tanner Tools Pro (A complete VLSI Design system)	16 th	16 th Nov 2006	BIET Davanagere
8	STTP on Advanced control Engg using MATLAB	26 th	30 th march 2007	Dr Ambedkar Institute of Technology, Bangalore
9	One day workshop on Nanotechnology			BMSCE, Bangalore
10	National Level Faculty training Programme on Mechatronics	2 nd	13 th Jan 2007	Sou Venutai Chavan college, Pune
11	Satellite communication	15 th	15 th March 2007	UBDTCE, Davanagere

12	Advances in VLSI Design	13 th	14 th march 2007	UBDTCE Davanagere
13	International Microelectronics & Packaging Society India, National Conference 2007	23 rd	24 th Nov2007.	BMSCE, Bangalore
14	Paper Presented in Innovations in Electronics & Information Technology ,National Conference on Design of DES Algorithm using FPGA	5 th	6 th Oct 2009	Vidyalankar Institute of Technology, Mumbai
15	Linear algebra and its applications	24 th	28 th Jan 2011	JNNCE, Shimoga
16	Workshop on 'Engineering Thermodynamics'	11 th Dec	21 st Dec 2012	IIT Bombay
17	Workshop on LATEX & MATLAB	23 rd July	27 th July, 2012	JNNCE, Shimoga
18	ISTE Workshop on 'Introduction to Research Methodologies'	25 th June	4 th July, 2012	IIT, Bombay
19	Signals & Systems	2 nd	12 th Jan 2014	IIT Kharagpur
20	Recent Progress & challenges ahead to assure quality in engineering education.	24 th	29 march 2014	VTU Belgaum(UBDTCE)
21	Computer Programming	20 th may	21 th June 2014 (2 weeks)	IIT Bombay
22	Advances in Biomedical Instrumentation for Quality Health Care	23 rd	26 th July 2014	UBDTCE Davanagere
23	Control Systems	2 nd	12 th Dec 2014	IIT Kharagpur

Section- III: Research and Publications (Feb 2003 to Feb 2008)

11. Doctoral Research	Registered for PhD On 2015 and completed course work
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12. Research Guidance: Master Degree/Diploma Dissertation (February 2003 to Jan 2018)

Year	Name of the Academic Programme	Number of Students Guided Successfully
2003-2010	UG B.E.	18 Batches of four students
2010-18	UG BE PG M.Tech	16 UG Batches (4 students in a batch) 16 PG batches (1 student batch)

14. Details of Papers Presented in Seminars/Symposium/Conference (Feb2003 to Feb 2011)

Name of Programme Date	Organizer	Title of the Paper	State/Regional/National/ International Level	Remarks (Key-note Address/ Chairing Session/ Rapporteur etc.,)
Innovations in Electronics & Information Technology	5 th oct 2009	Design of DES Algorithm using FPGA	Mumbai	--

15. Membership in Editorial Boards (Feb 2003 to Feb 2011): Chaitra college Magazine

16. Memberships in Academic Societies/Bodies (Feb 2003 to Feb 2011)

Name of Society	Affiliation	Position	Remarks
ISTE	--	Life member(LM 25261)	
IIE India		Member (M-148262-3)	

Section –V: Administrative and other Responsibilities**17. Chairmanship/Membership in University Academic Bodies, PG Level (BOE/BOS)**

1. BOE Chairman during 2004-05.
2. BOE Member during 2003-04.
3. Office superintendent in March/April 2003 Examination.
4. BOE Chairman, B E Course 2009-10
5. BOE Member, M.Tech course 2009-10
6. BOAE Member 2009-10
7. Warden Ladies Hostel 2009-12
8. Member Co-operative society: 2009- 2011
9. Member sports committee: 2010-2013
10. BOE member, VTU, EC Board, 2012-13
11. Member RUSA Committee 2013-2014
12. BOE Coordinator, EC Board, VTU. June-July 2013
13. Member AICTE Seat Matrix committee: 2011-2012
14. EMS Coordinator, E&CE Dept, 2013-14
15. Secretary, Committee on Women's Security and Grievance Redressal. 2013-14
16. Member AICTE Expert Committee: 2016 till date

18. Organization of Programmes (Seminar/Workshops,etc.) (Feb 2003 to Feb 2011)

Programme and Level (National, etc.,)	Date	Position Held	Remarks (Target Group, Number, etc.,)
Two day Workshop on "Software Engg Skills" Sponsored TEQIP	23&24March 2007	Co-ordinator	

19. Membership in Employees Association

Name of the Association	Position held	Period	
		From	To
UBDT staff council		AUG 1997	TILL DATE