

Dept of Studies & Research in Electronics School of Physical Sciences Jnanasahyadri, Shankaraghatta-577451, Shimoga (D)

Advertisement for the post of project fellow

Applications are invited for the post of project fellow to work under Research Project sponsored by UGC entitled "Digital Image analysis and denoising using Time-frequency techniques" The terms and conditions are applicable as per the UGC and University guidelines.

Principal Investigator : Dr. B.N. Jagadale

Assistant professor, Dept. of Electronics Jnana Sahyadri, Kuvempu University

Shankaraghatta-577451

Shimoga (D), Karnataka, India.

Mobile: 9886419329

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Fellowship : Project Fellow -Rs 14,000/- p.m (Non Gate/Non NET)/

Rs.16,000/-p.m (for GATE/NET/ GPAT)

No. of Fellowships : 01 (one)

Duration of the project : 2.5 years

Qualification : M.Sc. in Electronics/ (with at least 55%

marks). Preference will be given to NET qualified

candidate

Age limit : 28 years (Relaxation is admissible in case

of SC/ST/OBC as per UGC Regulation)

Date of interview : Will be intimated to the eligible candidates

and no TA and DA will be paid for appearing

the interview.

Application on plain paper with full bio-data (including address, e-mail ID, documents supporting to the qualifications and contact phone number etc.) should reach principal investigator on or before 12th March. 2016.

Dr. B.N. Jagadale Principal Investigator